


<b>PCN Number:</b>	20180328000	<b>PCN Date:</b>	April 3, 2018
<b>Title:</b>	Datasheet for TMS320F28374D - TMS320F28379D		
<b>Customer Contact:</b>	<a href="#">PCN Manager</a>	<b>Dept:</b>	Quality Services
<b>Change Type:</b>			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design
<input type="checkbox"/>	Assembly Process	<input checked="" type="checkbox"/>	Data Sheet
<input type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process
		<input type="checkbox"/>	Wafer Bump Site
		<input type="checkbox"/>	Wafer Bump Material
		<input type="checkbox"/>	Wafer Bump Process
		<input type="checkbox"/>	Wafer Fab Site
		<input type="checkbox"/>	Wafer Fab Materials
		<input type="checkbox"/>	Wafer Fab Process
<b>Notification Details</b>			
<b>Description of Change:</b>			
<p>Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.</p>			
		<b>TMS320F28379D, TMS320F28377D</b> <b>TMS320F28376D, TMS320F28375D, TMS320F28374D</b> <small>SPRS880I – DECEMBER 2013 – REVISED MARCH 2018</small>	
<b>Changes from July 25, 2017 to March 14, 2018 (from H Revision (July 2017) to I Revision)</b>			
			<b>Page</b>
<ul style="list-style-type: none"> <li>• <a href="#">Global</a>: Globally removed TMS320F28378D. .... <a href="#">1</a></li> <li>• <a href="#">Section 1.1 (Features)</a>: Added "Unique Identification Number" feature. .... <a href="#">1</a></li> <li>• <a href="#">Section 1.1</a>: Added "Configurable Logic Block (CLB)" feature. .... <a href="#">1</a></li> <li>• <a href="#">Table 3-1 (Device Comparison)</a>: Changed Conversion Time for ADC 12-bit mode from 290 ns to 280 ns. .... <a href="#">7</a></li> <li>• <a href="#">Table 5-1 (Device Current Consumption at 200-MHz SYSCLK)</a>: Changed "Operational (RAM)" to "Operational". Updated I<sub>DD</sub> MAX values. Added footnote about brownout events. .... <a href="#">52</a></li> <li>• <a href="#">Section 5.6 (Electrical Characteristics)</a>: Added V<sub>HYSTERESIS</sub>. .... <a href="#">56</a></li> <li>• <a href="#">Table 5-11 (PLL Lock Times)</a>: Updated footnote. .... <a href="#">64</a></li> <li>• <a href="#">Table 5-20 (Flash Parameters)</a>: Changed "Erase Time at 50k cycles" to "Erase Time at 20k cycles". .... <a href="#">69</a></li> <li>• <a href="#">Section 5.9 (Analog Peripherals)</a>: Updated list of analog subsystem features. .... <a href="#">95</a></li> <li>• <a href="#">Section 5.9.1.1 (ADC Configurability)</a>: Added section. .... <a href="#">100</a></li> <li>• <a href="#">Table 5-42 (ADC Operating Conditions (16-Bit Differential Mode))</a>: Added footnote about V<sub>REFCM</sub> requirements. ... <a href="#">102</a></li> <li>• <a href="#">Section 5.9.1.2.2 (ADC Timing Diagrams)</a>: Updated section. .... <a href="#">108</a></li> <li>• <a href="#">Table 5-50 (ADC Timing Parameters)</a>: Added table. .... <a href="#">108</a></li> <li>• <a href="#">Figure 5-49 (ePWM Submodules and Critical Internal Signal Interconnects)</a>: Updated figure. .... <a href="#">126</a></li> <li>• <a href="#">Section 5.11.2.1 (I2C Electrical Data and Timing)</a>: Added NOTE about meeting I2C protocol timing specifications. .... <a href="#">142</a></li> <li>• <a href="#">Table 5-72 (McBSP as SPI Master or Slave Switching Characteristics (CLKSTP = 10b, CLKXP = 0))</a>: Updated table title. .... <a href="#">148</a></li> <li>• <a href="#">Table 5-74 (McBSP as SPI Master or Slave Switching Characteristics (CLKSTP = 11b, CLKXP = 0))</a>: Updated table title. .... <a href="#">149</a></li> <li>• <a href="#">Table 5-76 (McBSP as SPI Master or Slave Switching Characteristics (CLKSTP = 10b, CLKXP = 1))</a>: Updated table title. .... <a href="#">150</a></li> <li>• <a href="#">Table 5-78 (McBSP as SPI Master or Slave Switching Characteristics (CLKSTP = 11b, CLKXP = 1))</a>: Updated table title. .... <a href="#">151</a></li> <li>• <a href="#">Section 5.11.6 (Universal Serial Bus (USB) Controller)</a>: Updated list of USB module features. .... <a href="#">168</a></li> <li>• <a href="#">Table 6-9 (Device Identification Registers)</a>: Changed UID_UNIQUE from "0x0007 03C0" to "0x0007 03CC". .... <a href="#">185</a></li> <li>• <a href="#">Figure 6-5 (Windowed Watchdog)</a>: Updated figure. .... <a href="#">198</a></li> <li>• <a href="#">Section 8.2 (Tools and Software)</a>: Added "Models" section. .... <a href="#">201</a></li> <li>• <a href="#">Section 8.3 (Documentation Support)</a>: Updated section. .... <a href="#">203</a></li> </ul>			
The datasheet number will be changing.			
Device Family		Change From:	Change To:
TMS320F28374D - TMS320F28379D		SPRS880H	SPRS880I
These changes may be reviewed at the datasheet links provided.			
<a href="http://www.ti.com/product/TMS320F28379D">http://www.ti.com/product/TMS320F28379D</a>			
<b>Reason for Change:</b>			
To accurately reflect device characteristics.			
<b>Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):</b>			
No anticipated impact. This is a specification change announcement only. There are no changes			

to the actual device.

**Changes to product identification resulting from this PCN:**

None.

**Product Affected:**

TMS320F28374DPTPS	TMS320F28375DPZPS	TMS320F28376DZWTT	TMS320F28377DZWTS
TMS320F28374DPTPT	TMS320F28375DZWTS	TMS320F28377DPTPQ	TMS320F28377DZWTT
TMS320F28374DZWTS	TMS320F28375DZWTT	TMS320F28377DPTPS	TMS320F28379DPTPS
TMS320F28374DZWTT	TMS320F28376DPTPS	TMS320F28377DPTPT	TMS320F28379DPTPT
TMS320F28375DPTPS	TMS320F28376DPTPT	TMS320F28377DZWTQ	TMS320F28379DZWTS
TMS320F28375DPTPT	TMS320F28376DZWTS	TMS320F28377DZWTQR	TMS320F28379DZWTT

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